

V1120

Virtex[®]-5 FPGA XMC

Benefits

Combination of high-performance FPGA and fast memory provides a powerful processing platform.

Provides a bridge between PCI Express and 10 Gigabit Ethernet in highly- ruggedized systems.

Features

User programmable Xilinx Virtex-5 LX110T, LX155T or SX95

Built-in UDP offload engine for real time communication

Supports PCI Express and XAUI host interfaces

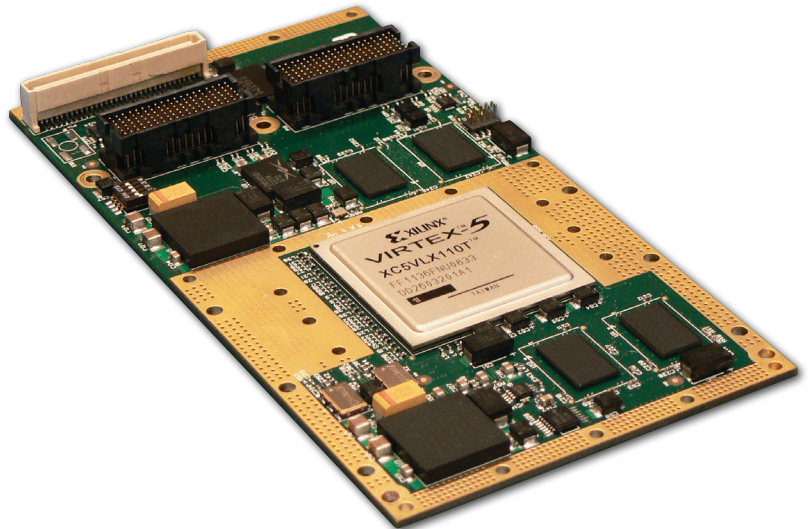
High performance SDRAM memory architecture

Thermal sensors for monitoring card temperature

Available in air and conduction-cooled XMC form factor

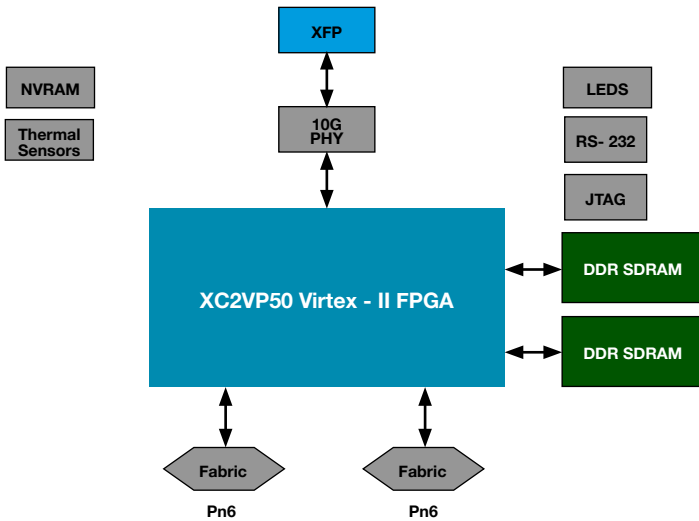
Overview

The V1120 conduction-cooled XMC with its high performance Xilinx Virtex-5 FPGA and memory architecture is intended to provide additional processing capabilities in embedded systems. It is ideal for highly ruggedized radar and signal intelligence applications. The V1120 comes standard with the expressXG Development Framework, an advanced infrastructure that gives programmers the advantage to develop their own high bandwidth applications rapidly and efficiently. It supports PCI Express on Pn5 connector and XAUI on Pn6 connector. The V1120 is also available with udpXG, a high performance UDP offload capabilities for real time 10 Gigabit Ethernet communications.



V1120

Virtex[®]-5 FPGA XMC



> 1120 Virtex-5 FPGA XMC Block Diagram

Technical Specifications

FPGA DEVICE

Xilinx Virtex-5 - LX110T, LX155T or SX95T

MEMORY

2 banks of 0.5GB or 1GB 64-bit 266MHz DDR2 SDRAM

FLASH

2 32Mb memory for storing a default configuration image and a recovery image

HOST INTERFACE

PCI Express x8 or XAUI on Pn5
XAUI on Pn6

EXTERNAL INTERFACE

42 single-ended user I/O on Pn4

THERMAL SENSORS

2 digital temperature sensors

COMPLIANCE

VITA 42.3
VITA 42.6

DIMENSIONS

74 mm (width) x 149 mm (length)

POWER REQUIREMENTS

Maximum 12W

ENVIRONMENTAL

Operating: -40° C to 85° C
Storage: -55° C to 125° C

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